



Patent

Case No.: 55271US002

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: GEISSINGER, JOHN D.

Application No.:

09/491,302

Group Art Unit:

2815

Filed:

25 January 2000

Examiner:

Paul E. Brock, II

Title:

ELECTRONIC PACKAGE WITH INTEGRATED CAPACITOR

## RESPONSE TO FINAL OFFICE ACTION DATED APRIL 1, 2003

Commissioner for Patents Washington, DC 20231

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents,

Washington, DC 20231 on:

Dear Sir:

This response is to the Final Office Action with the mailing date of April 01, 2003.

**AMENDMENTS** 

Please amend this application as follows:

In the Claims:

Please amend claim(s) 1 and 13 as follows:

(Four times Amended). 1.

An electronic package, comprising:

a conductive trace layer having a first side and a second side, the conductive trace layer being patterned to define a plurality of interconnect pads;

a dielectric substrate mounted on the first side of the conductive trace layer;